

Dual Complementary Pair Plus Inverter

The MC14007UB multi-purpose device consists of three N-channel and three P-channel enhancement mode devices packaged to provide access to each device. These versatile parts are useful in inverter circuits, pulse-shapers, linear amplifiers, high input impedance amplifiers, threshold detectors, transmission gating, and functional gating.

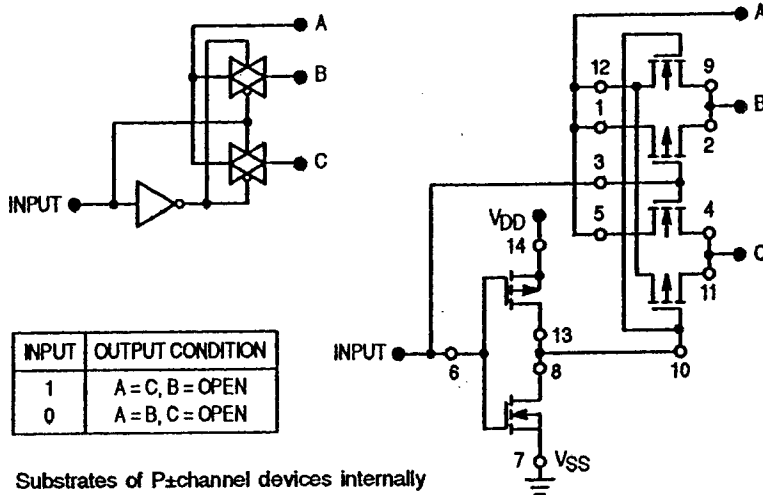
- Diode Protection on All Inputs
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Capable of Driving Two Low-power TTL Loads or One Low-power Schottky TTL Load Over the Rated Temperature Range
- Pin-for-Pin Replacement for CD4007A or CD4007UB
- This device has 2 outputs without ESD Protection. Anti-static precautions must be taken.

MAXIMUM RATINGS* (Voltages Referenced to V_{SS})

Symbol	Parameter	Value	Unit
V _{DD}	DC Supply Voltage	± 0.5 to + 18.0	V
V _{in} , V _{out}	Input or Output Voltage (DC or Transient)	± 0.5 to V _{DD} + 0.5	V
I _{in} , I _{out}	Input or Output Current (DC or Transient), per Pin	± 10	mA
P _D	Power Dissipation, per Packages	500	mW
T _{stg}	Storage Temperature	± 65 to + 150	°C
T _L	Lead Temperature (8-Second Soldering)	260	°C

* Maximum Ratings are those values beyond which damage to the device may occur. ≤ Temperature Derating:

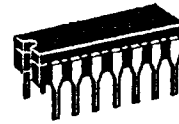
Plastic [™]P and D/DW/J Packages: ± 7.0 mW/°C From 65°C To 125°C
 Ceramic [™]L/J Packages: ± 12 mW/°C From 100°C To 125°C



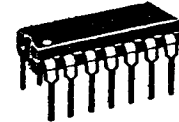
Substrates of P-channel devices internally connected to V_{DD}; substrates of N-channel devices internally connected to V_{SS}.

Figure 1. Typical Application: 2-Input Analog Multiplexer

MC14007UB



L SUFFIX
CERAMIC
CASE 632



P SUFFIX
PLASTIC
CASE 646



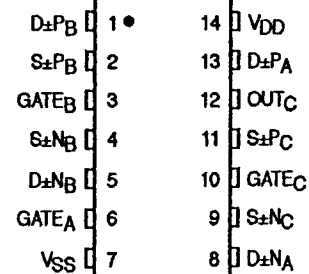
D SUFFIX
SOIC
CASE 751A

ORDERING INFORMATION

MC14XXXUBCP Plastic
 MC14XXXUBCL Ceramic
 MC14XXXUBD SOIC

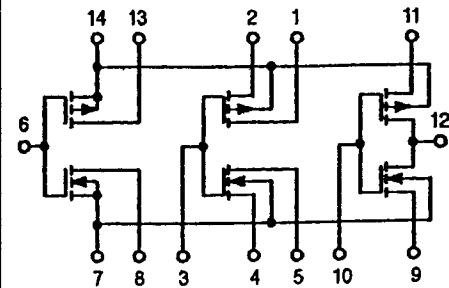
T_A = ± 55° to 125°C for all packages.

PIN ASSIGNMENT



D = DRAIN
S = SOURCE

SCHEMATIC



V_{DD} = PIN 14
V_{SS} = PIN 7