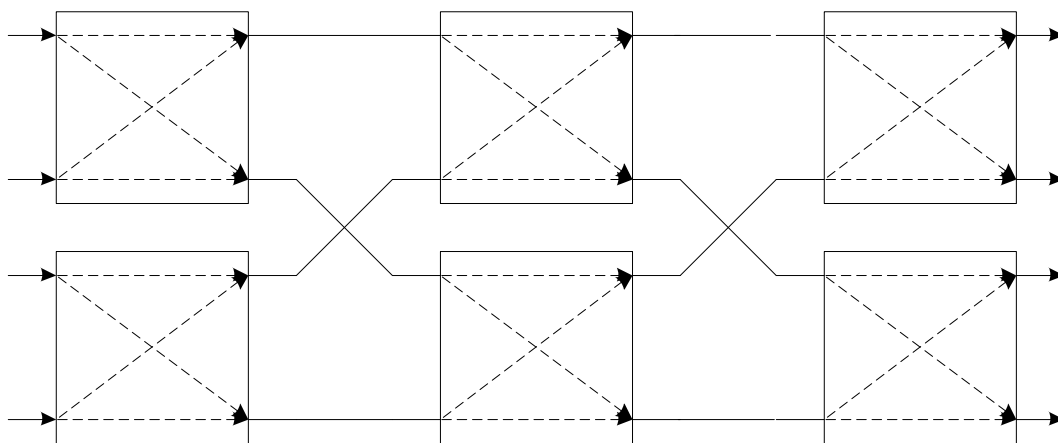
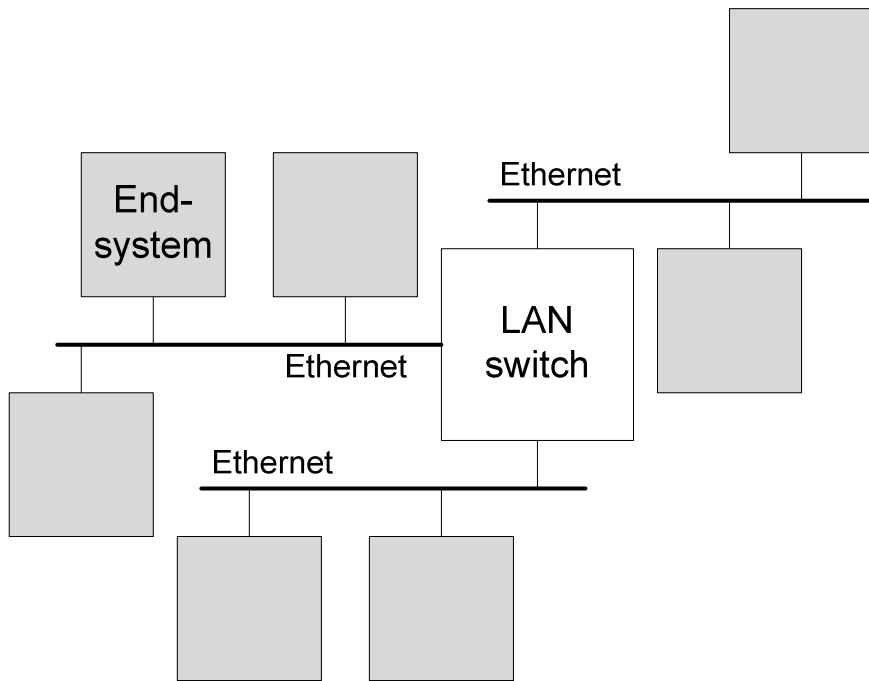
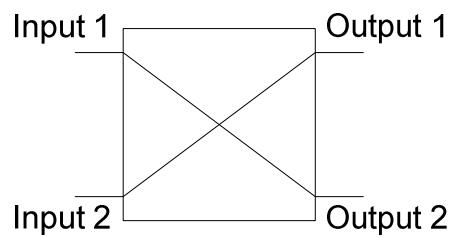


(a)

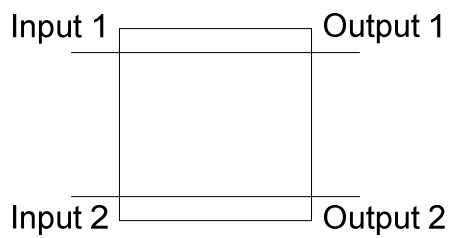


(b)



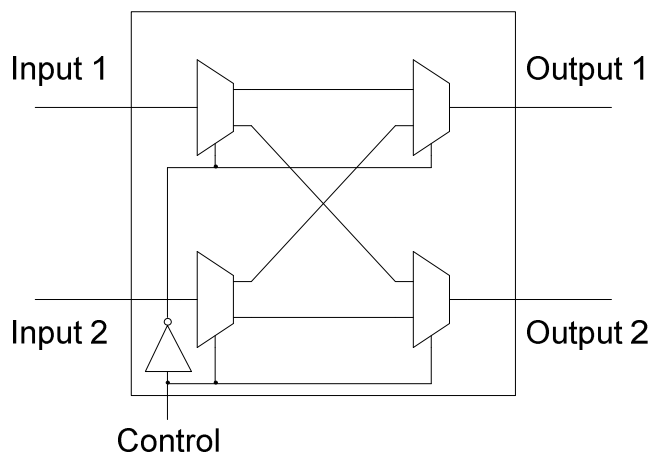


“Cross” configuration

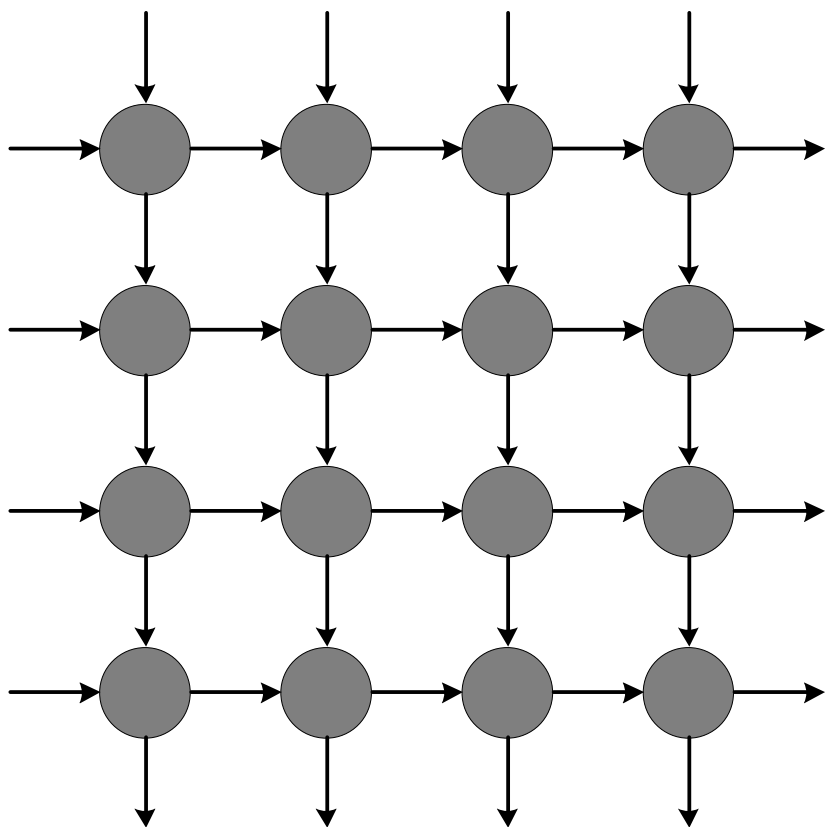


“Bar” configuration

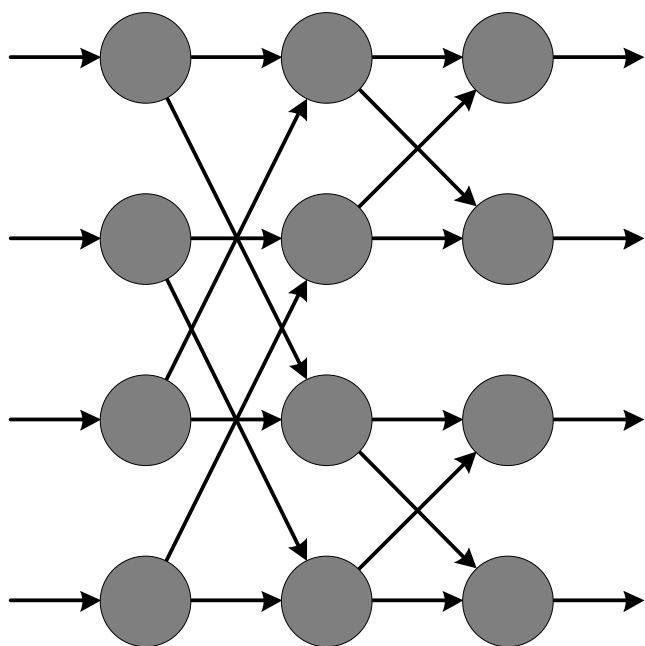
(a)



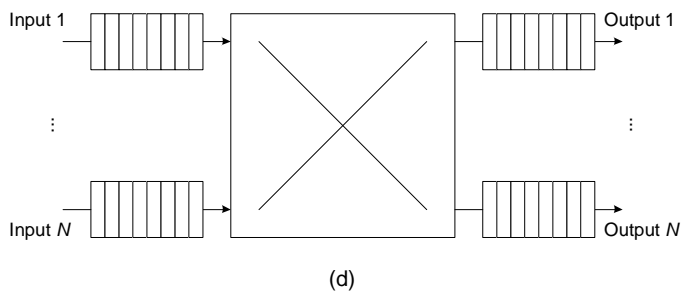
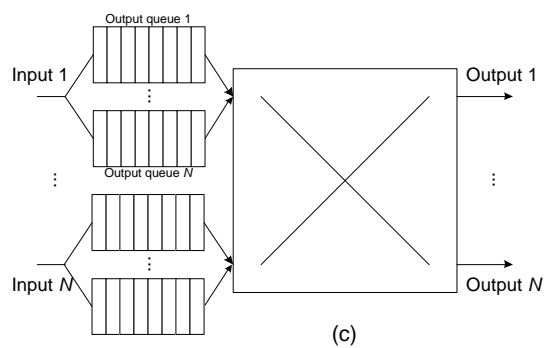
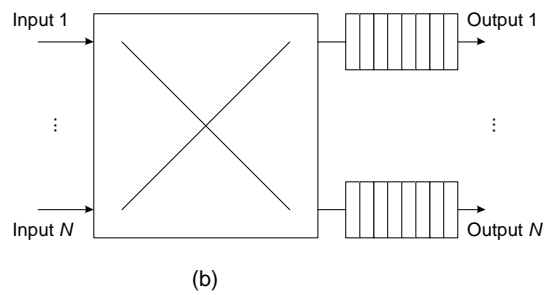
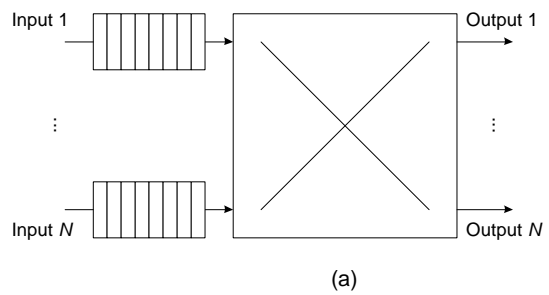
(b)

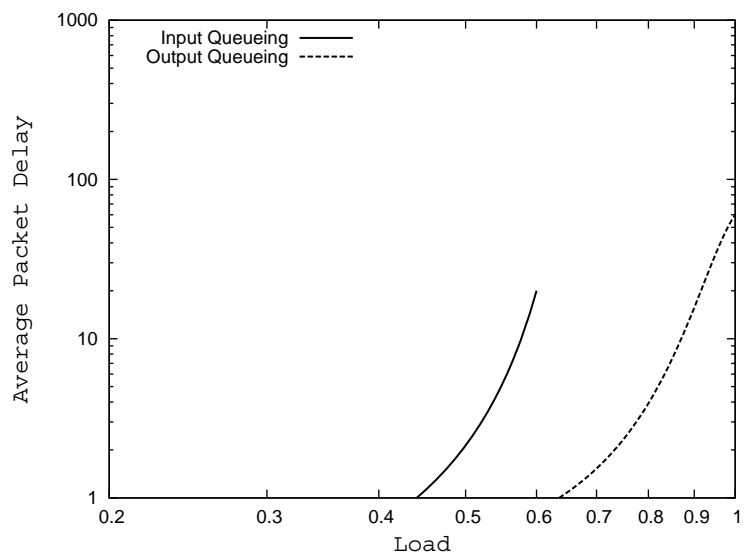


(a)



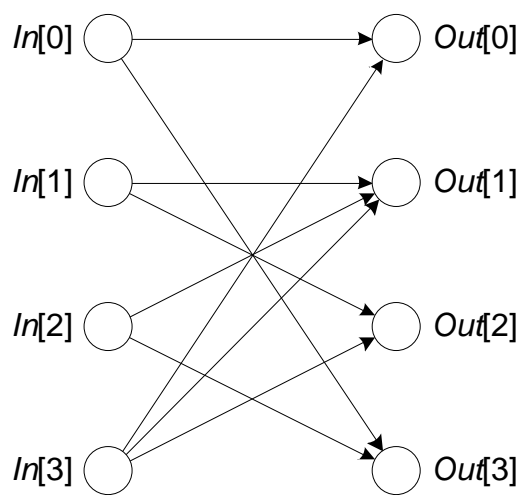
(b)





		Outputs			
		0	1	2	3
Inputs	0	1	0	0	1
	1	0	1	1	0
	2	0	1	0	1
	3	1	1	1	0

(a)



(b)

	$Out[0]$	$Out[1]$	$Out[2]$	$Out[3]$
$In[0]$	D_0	D_1	D_2	D_3
$In[1]$	D_3	D_0	D_1	D_2
$In[2]$	D_2	D_3	D_0	D_1
$In[3]$	D_1	D_2	D_3	D_0

(a)

Switch cycle		→			
		0	1	2	3
Diagonal sequence ↓	0	0	1	2	3
	1	1	3	0	2
	2	2	0	3	1
	3	3	2	1	0

(b)

