

Fig. 1. A combinational network.

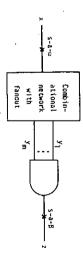


Fig. 2. A reconvergent fanout line.

Index Terms—Backward tracing, combinational network, reconvergent famout line, sensitized path, unsensitized propagating line.

Fault detection by path sensitization is a well-known technique which has been studied extensively in the literature [1], [2]. A sensitized path is informally defined as a path in a digital network leading from the site of a fault to a network output and where the network inputs are specified in such a manner so as to generate the appropriate value at the site of the fault and to propagate the fault signal along this path to the output [1], [2]. In most cases the faults that occur along a sensitized path will also be detected at the network output.

In this correspondence we investigate the conditions under which a test will propagate a fault through paths in a network without sensitizing the entire paths [3]. We shall consider combinational networks with single stuck-at type faults. As an example consider the network in Fig. 1 [3]. The test $x_1 x_2 x_3 = 111$ detects the faults x_2 s-a-0 and x_6 s-a-0, but does not detect any of the faults on either line 4 or line 5, which are clearly part of the propagating paths from line 2 to line 6. We shall subsequently refer to such lines, which propagate faults to the output without being sensitized, as unsensitized propagating lines.

The detection of unsensitized propagating lines is very important in backward tracing when specifying the faults covered by a given test [4]. In the backward-tracing approach, once an unsensitized line is reached the subnetwork feeding this line can usually be ignored, except when the unsensitized lines are unsensitized propagating lines. In such a case further backward tracing within the unsensitized subnetwork must be performed. The use of backward tracing for specifying the faults covered by a given test is usually preferable to forward simulation techniques (parallel or deductive simulation). While in the forward simulation all paths in the network emanating from the primary input lines must be checked; in the backward tracing operation only the sensitized paths and the unsensitized propagation lines must be checked. Hence, the required computation time is considerably smaller.

As will be shown later, the existence of unsensitized propagating lines is closely related to the existence of reconvergent fanout lines within the network. Let x be a fanout line and let z denote the output of the reconvergence gate whose input lines are y_1 , y_2 , ..., y_m , as depicted in Fig. 2. This reconvergence gate and all other gates in the network are assumed to be conventional gates like and, or, not, nand, nor. Such a gate can be uniquely

On the Properties of Sensitized Paths

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Abstract—In this note we investigate the conditions under which the paths which propagate a fault in a combinational network to the network output are not necessarily entirely sensitized.

Manuscript received September 23, 1977; revised January 16, 1978.

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We shall refer to such a vector as the describing vector for the gate. For example, a three-input NAND gate is described by (1, 1, 1, 0). described by a binary vector $(\gamma_1, \gamma_2, \dots, \gamma_m, \delta)$, where $\gamma_1, \dots, \gamma_m$ is the only input combination for which the output z equals δ [4].

paths is a necessary condition for the detection of $x \cdot s - a - \alpha$. of the propagating paths is the same, the fault will propagate to z. detected by the test [1]. On the other hand, if the inversion parity not the same, the fault will not propagate to z and thus will not be Clearly, if the number of inversions along the path is even (odd), then $\alpha = \beta$ ($\alpha = \overline{\beta}$). If, however, for a given test T the fault propafor s-a- β . In this case all the lines along the path are sensitized. single path. Suppose also that the same test can be used to test z suppose that the fault propagates to the output z through just a possible cases. If the inversion parity of the propagating paths is gates through two or more paths leading from x to z, we have two Let T be a given test that covers the fault x s-a- α and initially equality of the inversion parities along the propagating

contains unsensitized propagating lines iff: Theorem 1: A sensitized subnetwork corresponding to a test T

- fault propagates to the output of a reconvergence gate through at least two paths, all having the same inversion parity; and 1) The test T covers a fault s-a- α at a fanout point x and this
- gence gate, such that $\beta = \delta$. 2) the test T covers a fault s-a- β at the output z of the reconver-

line, $y_j \neq \gamma_j$. (For example, if the reconvergence gate is a four-input AND gate whose describing vector is (1 1 1 1 1), the fault covered by test T is z s-a-1 and for the fault-free output we have output z. Thus, it follows that neither of these lines is sensitized are at least two input lines to the reconvergence gate for which $z = \overline{b} = 0$. Clearly, a fault can propagate from x to z only through the input lines satisfying $y_j = 0 \neq \gamma_j$.) Now, by condition 1, there must satisfy $y_i \neq y_j$. Clearly, for each line y_j to be a propagating fault-free network is $z = \overline{\delta}$. Hence, by the definition of the describ $y_j \neq y_j$ and a single fault in one of them will not propagate to the ing vector, at least one of the input lines to the reconvergence gate covers the fault z s-a- β and since $\beta = \delta$, the output z for the Proof: Suppose the two conditions above are satisfied. If T

are two or more input lines for which $y_j \neq y_j$, a single fault in one satisfies $y_i \neq y_j$, this line is sensitized. If, on the other hand, there occurs only if all the y_i 's for which $y_i \neq \gamma_i$ are on paths emanating from a fanout point x and all these propagating paths have the $y_i \neq \gamma_j$, but does not affect the remaining y's. Such a situation gate from x to z iff it changes simultaneously all the y_j for which fault s-a- α from a preceding line x. In fact, such a fault will propanot sensitized. However, these unsensitized lines may propagate a of them will not propagate to the output. Hence, these lines are the inputs to the gate satisfies $y_j \neq y_j$. Clearly, if only one line also sensitized; and 2) $z = \bar{\delta}$ and $\beta = \delta$. In this case at least one of $y_m = y_1 \cdots y_m$, and consequently each of the input lines $y_1 \cdots y_m$ is z for the fault-free network is $z = \delta$, i.e., $\beta = \overline{\delta}$. the network. There are two cases to be considered: 1) The output that the test T covers a fault s-a- β at the output line z of a gate in To prove that the above conditions are also necessary, suppose Hence, y₁ ··· Q.E.D.

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Revision of the Buffer Length Derivation for a Modified $E_k/D/1$ Systems by Maritsas and Hartley

size is possible. The length of buffer required to reduce the overflow loss to an acceptably low level is an important design parameter. inputs and of similar equipments where only a limited input buffer limited length is relevant to the design of computers with real-time Abstract—The problem of loss of items arriving at a queue of

(Poisson) arrival rate and by Maritsas and Hartley [2] for an Erlang arrival rate. A discrepancy between the two models for an Erlang input of degree 1 led to a re-examination of the latter model.

This paper introduces a revised model for Erlang input. It gives the same results as Dor's model for degree 1, and substantially affects This problem has been considered by Dor [1] for a random

the results for degrees 2 and higher.

Index Terms—Buffer length, Erlang input/constant removal rate, fractional loss, queue length distribution.

INTRODUCTION

sampling intervals. If the input buffer is too small, the fractional if the queue is already full when an item arrives. In many equiploss of input items may be unacceptable. size from which they are removed in the order of arrival at regular ments, items arrive at random and are placed in a buffer of limited Items arriving at a queue of limited length are lost by overflow

published a model for Erlang input. These models will be referred (Poisson) arrivals, and subsequently Maritsas and Hartley [2] to as the Dor model and the Maritsas model in this paper, which A model for this situation was developed by Dor [1] for random

mation which, acting on an arbitrary set of probabilities q(W)the special case of q'(W) = q(W). q'(W) at the next sampling instant. The equilibrium condition is existing at a sampling instant, generates the set of probabilities will use the notation of (and assume a knowledge of) reference [2] The derivation of [2, eq. (8)] shows that it is in fact a transfor-

The transformation equation is therefore

$$q'(W) = \begin{cases} \sum_{r=0}^{W} q(W+K-r)p(r) + \sum_{r=0}^{\min(W,K-1)} q(r)p(W-r), \\ 0 \le W \le KL + K - M \le KL + K. \end{cases}$$

the author by successive application of (1) from an arbitrary initial set of q(W), normalizing q'(W) so that Solutions for the fractional loss R_L of items were obtained by

$$\sum_{W=0}^{KL+K-1} q'(W) = 1$$

normalization indicated that some of the transition probabilities using equation [2, eq. (11)] to find R_L . The results obtained agreed with those of reference [2] for K = 2, 3, 4, and 5, but were some three times greater for K = 1 than those of reference [1]. This gave after each application, until equilibrium was reached, and then in the Maritsas model were incorrect. the fact that the sum of all q'(W) was always less than 1 before rise to suspicion that there was an error in one of the models, and

To investigate this, a single application of (1) was performed for

Manuscript received December 1, 1976; revised November 1, 1977.

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