

ENGIN112 - Introduction to Electrical and Computer Engineering

Homework 5

Due 28 October 2009 at 1:25 p.m. (before class starts)

Please write down the course and homework number, and your name on all sheets. Answer the problems in the order they were listed in the assignment. Show all your work.

Solve the following problems.

1. 3.20
2. 4.1
3. 4.5
4. 4.9
5. 4.13
6. 4.15
7. Draw a CMOS circuit diagram for the function $F=(A(B+C))'$ as a single logic gate, without any inverters. Hint: You should have three transistors in both the "pull-up" and "pull-down" sections.